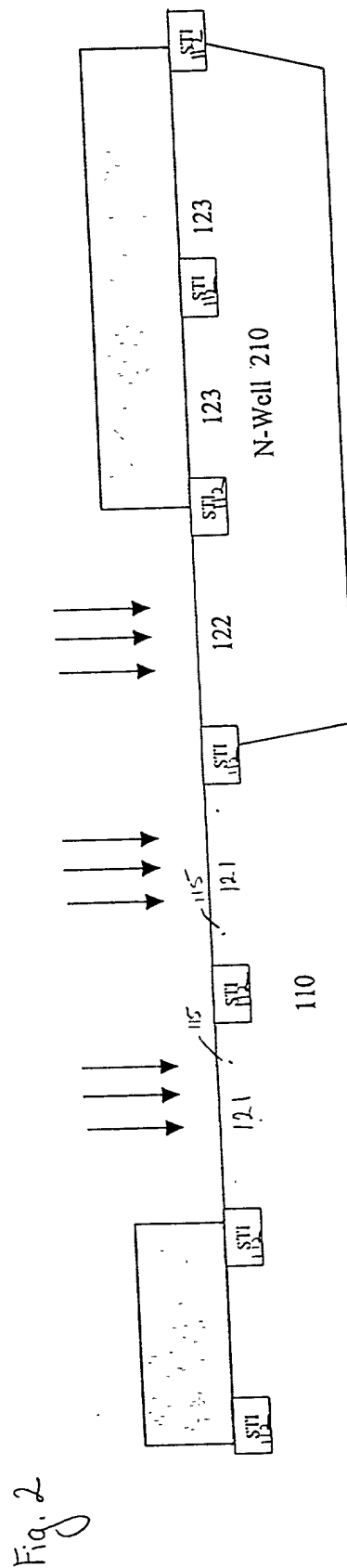
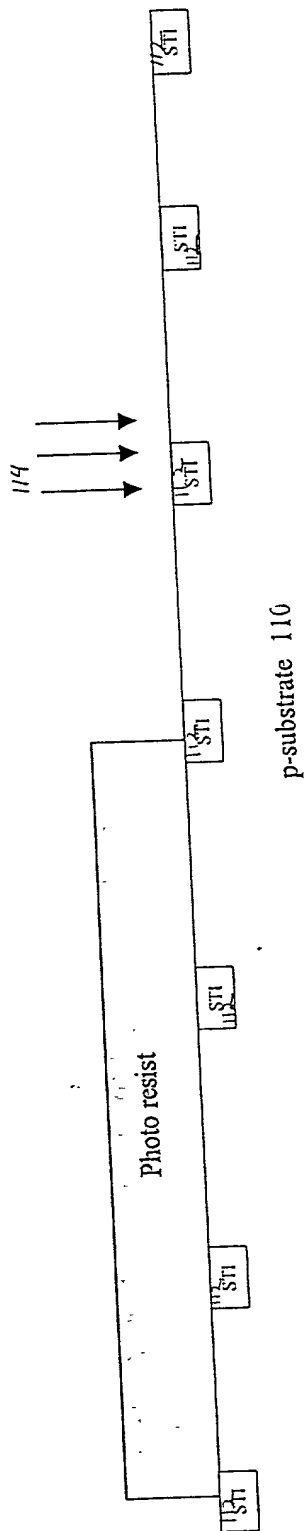


N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel 12.1	P-Poly NMOS Buried Channel 12.1	P-Poly PMOS Surface Channel 12.2	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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Fig. 3

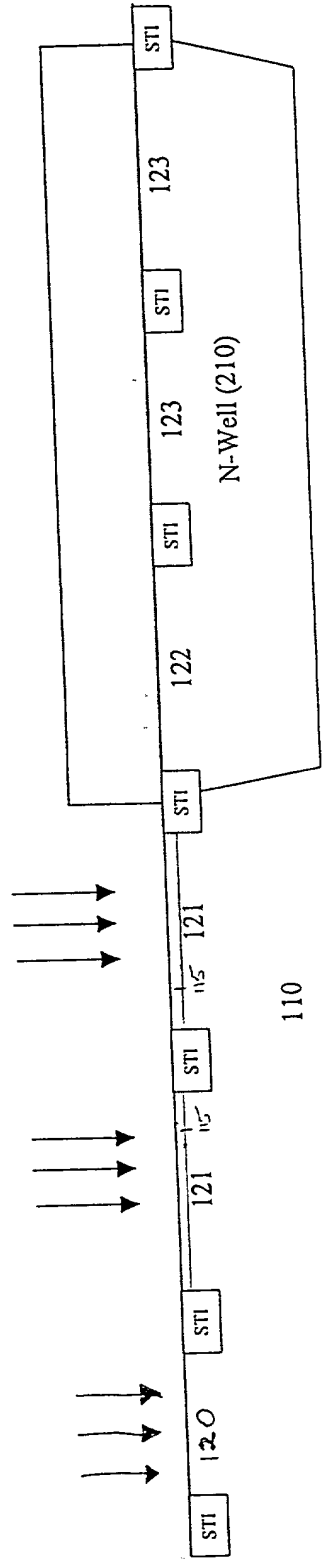
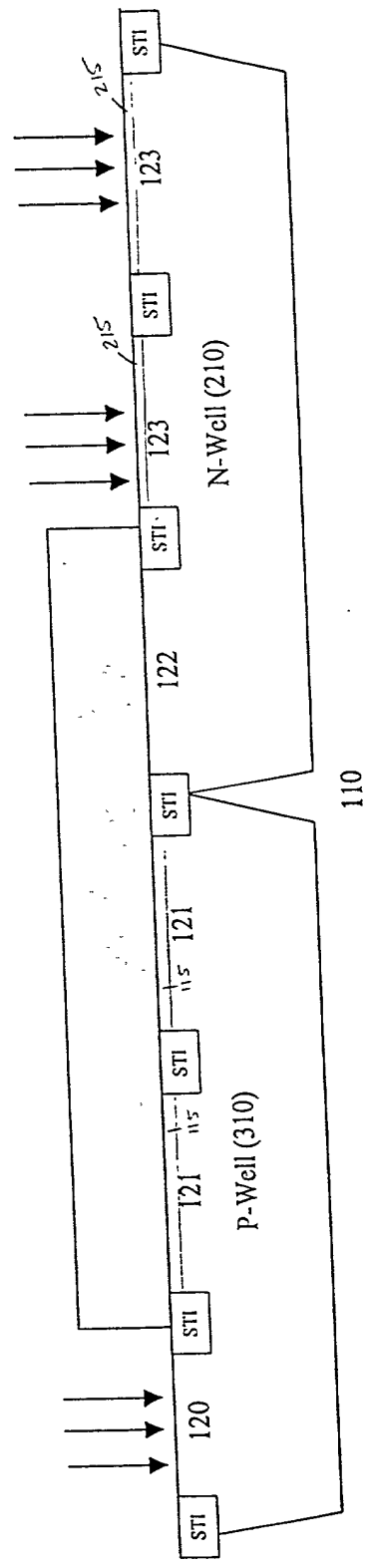


Fig. 4



N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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Fig. 7

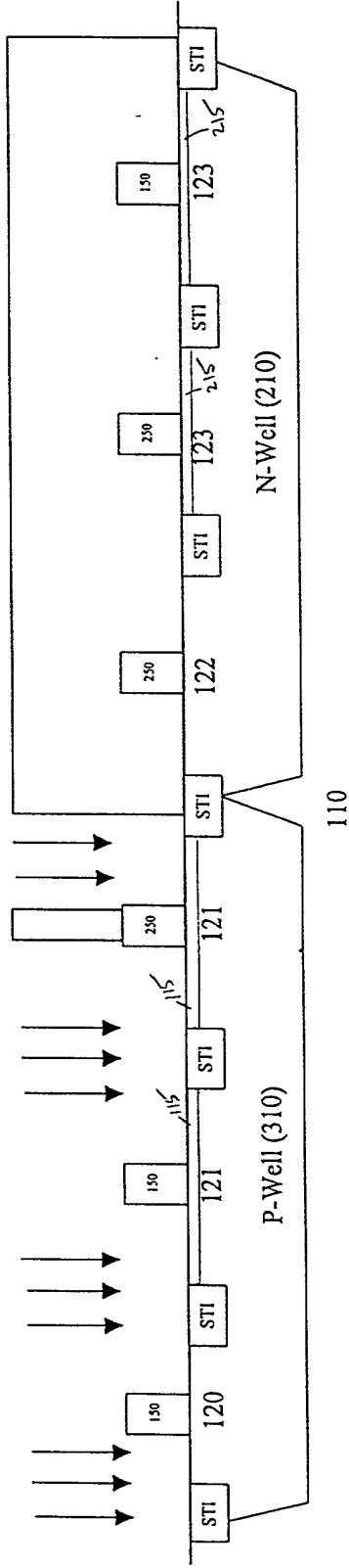
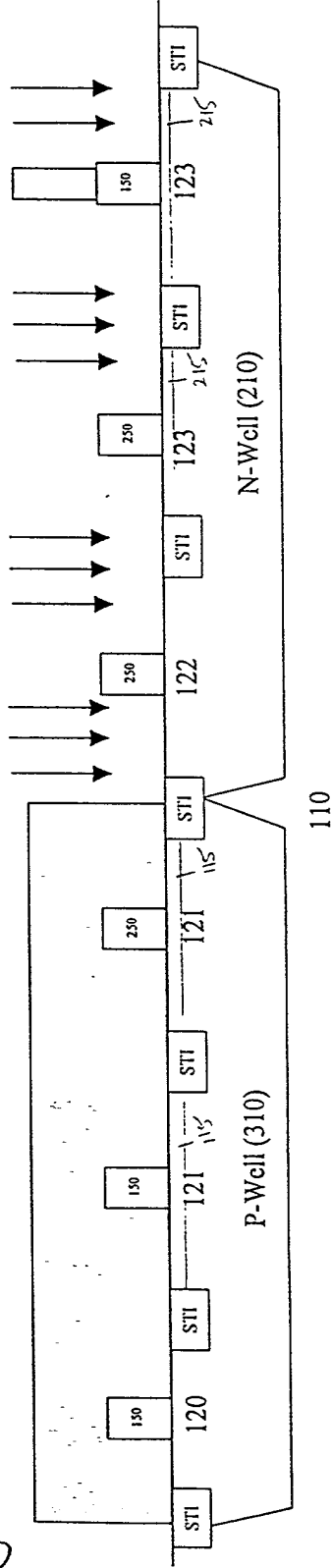


Fig. 8



N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel

Fig. 9a

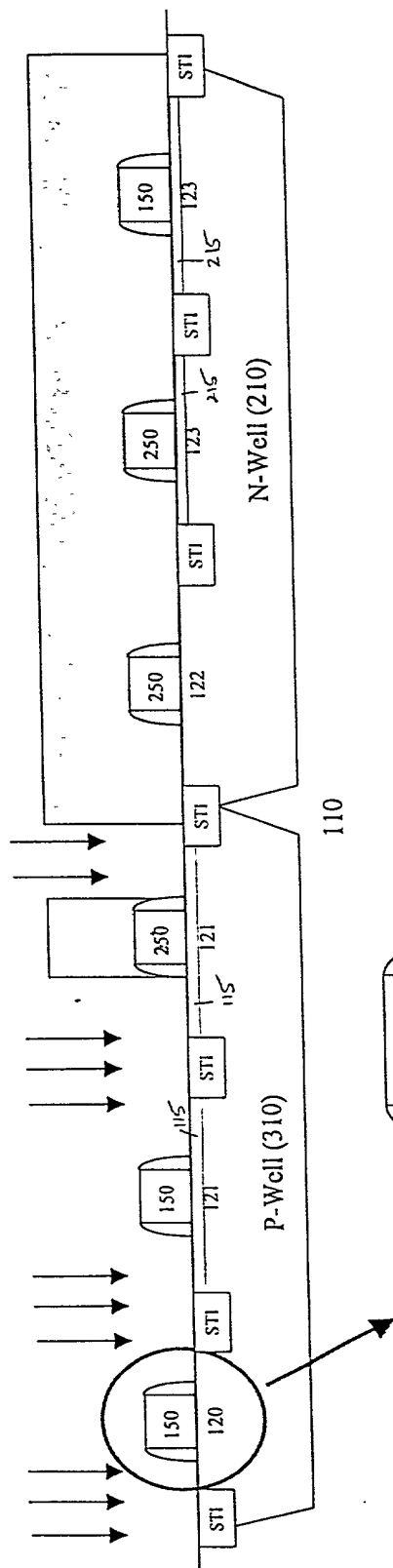
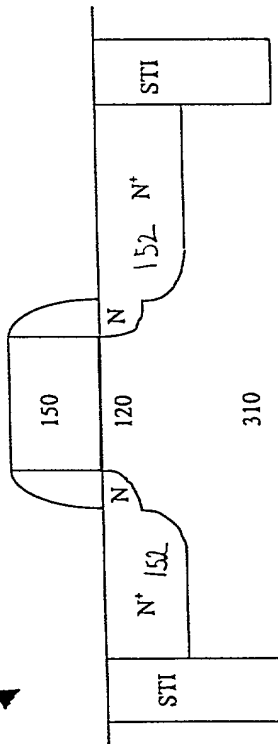


Fig. 9b



N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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Fig. 10a

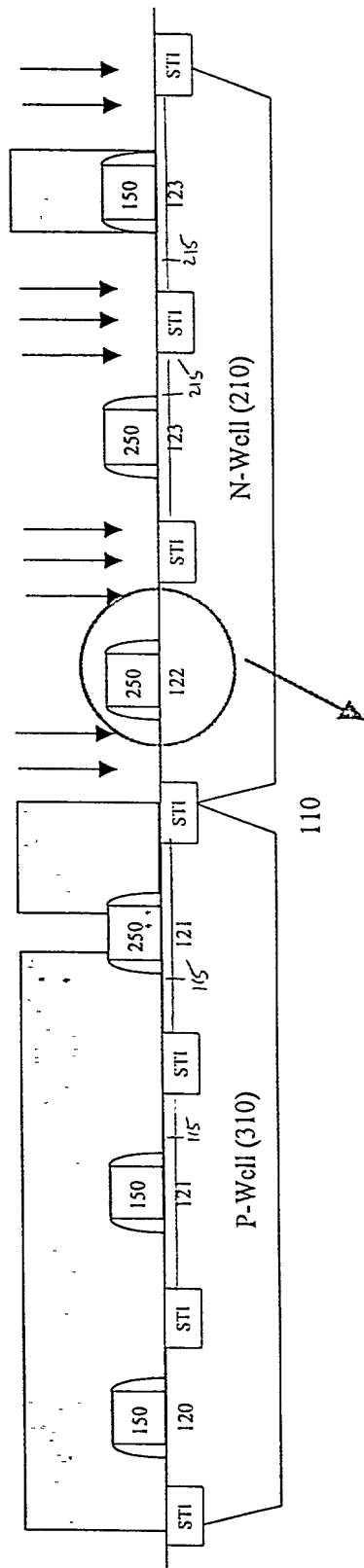


Fig. 10b

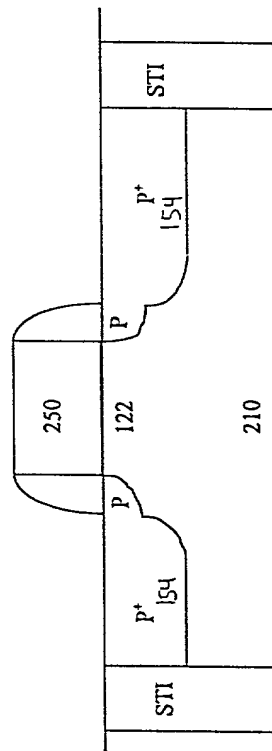


Fig. 11a (Prior Art)

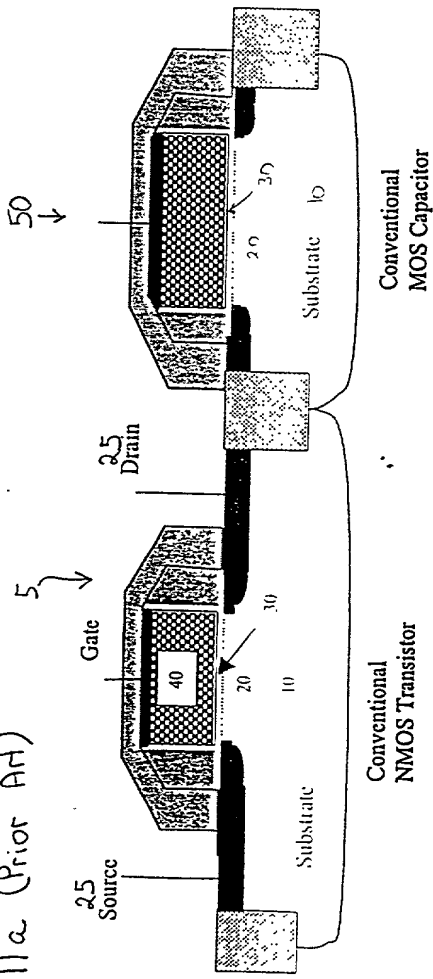


Fig. 11b

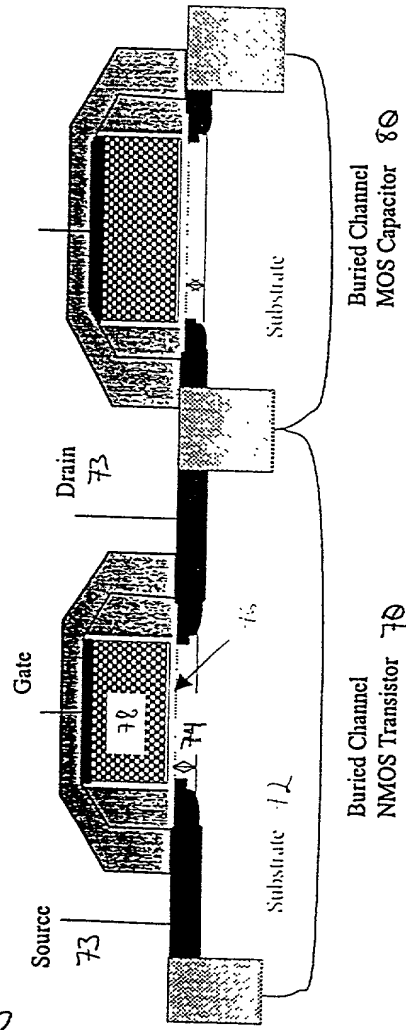


Fig. 12a
(Prior Art)

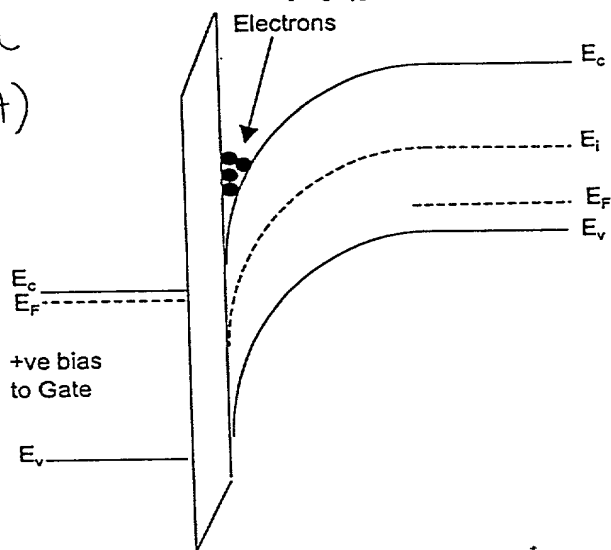


Fig. 12b

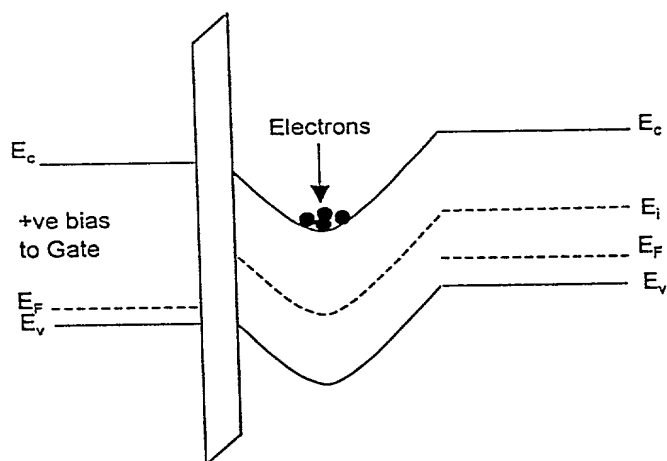
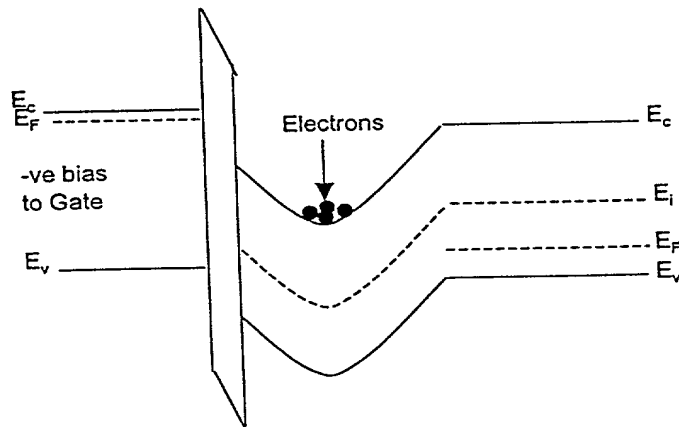


Fig 12c



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